



**INFORMATION DISCLOSURE STATEMENT FILING PROVISION:**

☒ This IDS is believed to be timely in that it is being submitted under 37 CFR § 1.97(b), that is (1) within three months of the filing date of the application, which is not a continued prosecution application filed under § 1.53(d); or (2) within three months of entry of the national stage as set forth in 37 CFR § 1.491; or (3) before the mailing of a first Office action on the merits; or (4) before the mailing of a first Office action after filing a request for continued examination under § 1.114. Thus, no fee is required.

☒ However, if the undersigned is in error in this regard, Applicant respectfully requests that the Office consider this IDS as filed under 37 CFR § 1.97(c), if applicable, and charge the fee due under 37 CFR § 1.17(p) to the deposit account referenced below.

☐ However, if the undersigned is in error in this regard, Applicant respectfully requests that the Office consider this IDS as filed under 37 CFR § 1.97(c), if applicable, and a statement under 37 CFR § 1.97(e) is included below, thus no fee is required.

☐ This IDS is being submitted under 37 CFR § 1.97(c), that is after mailing of a first Office action on the merits, but before a Final Action under 37 CFR § 1.113 or a Notice of Allowance under 37 CFR § 1.311.

☐ The fee due under 37 CFR § 1.17(p) is submitted herewith.

☐ A statement under 37 CFR § 1.97(e) is included below, thus no fee is required. In the event that this IDS is not received before a Final Action or a Notice of Allowance, then Applicant respectfully requests that the Office consider the filing of these papers to be submitted under 37 CFR § 1.97(d) and charge the fee due under 37 CFR § 1.17(p) to the deposit account below.

☐ This IDS is being submitted under 37 CFR § 1.97(d), that is after a Final Action under 37 CFR § 1.113 or a Notice of Allowance under 37 CFR § 1.311, but before payment of the issue fee. A statement under 37 CFR § 1.97(e) is included below. The fee due under 37 CFR § 1.17(p) is submitted herewith.

**STATEMENT UNDER 37 CFR § 1.97(e):**

☐ Each item contained in this IDS was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this IDS.

☐ No item contained in this IDS was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing this statement after making reasonable inquiry, no item of information contained in this IDS was known to any individual designated in 37 CFR § 1.56(c) more than three months prior to the filing of this IDS.

**PAYMENT AND/OR AUTHORIZATION TO CHARGE FEES:**

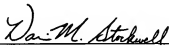
- ☐ A check in the amount of \_\_\_\_\_ is enclosed for the above fee(s).  
☐ Please charge \_\_\_\_\_ to Deposit Account No. **15-0665** for the above fee(s).

The Commissioner is authorized to charge any fees required by the filing of these papers, and to credit any overpayment to Orrick, Herrington & Sutcliffe's Deposit Account No. **15-0665**.

Respectfully submitted,  
Orrick, Herrington & Sutcliffe LLP

Dated: November 25, 2003

By:

  
Davin M. Stockwell  
Reg. No. 41,334

4 Park Plaza, Suite 1600  
Irvine, California 92614-2558  
Telephone: (949) 567-6700  
Facsimile: (949) 567-6710  
Customer No.: 34313

LIST OF PATENTS AND OTHER ITEMS FOR APPLICANT'S  
INFORMATION DISCLOSURE STATEMENT

## APPLICANT:

Michael R. Butts et al.

## FILING DATE:

July 3, 2003

## GROUP:

Not Yet Assigned

(Us s v ralshe ts if n c ssary)

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE
AA						
AB						
AF						
AD						
AF						
AF						
AG						
AH						

## FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION YES NO
AI						
AJ						
AK						

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

/HE/	AL	ProDesign GmbH, "CHIPit Gold Edition," Copyright 2003, 2 pages, available at <a href="http://www.chipit.de/ce/CHIPitGoldEdition_UMRbus.htm">http://www.chipit.de/ce/CHIPitGoldEdition_UMRbus.htm</a>
/HE/	AM	ProDesign GmbH, "Speeding Verification of Custom Designs," Embedded Systems Europe, June 2003, 2 pages, available at <a href="http://www.chipit.de/ce/pdf/whitepaper/Embedded%20Europe.pdf">http://www.chipit.de/ce/pdf/whitepaper/Embedded%20Europe.pdf</a>
/HE/	AN	ProDesign GmbH, "Crack the Bottleneck Problem," March 2002, 4 pages, available at <a href="http://www.chipit.de/ce/pdf/whitepaper/crack.pdf">http://www.chipit.de/ce/pdf/whitepaper/crack.pdf</a>
/HE/	AO	Aptix Corporation, "Module Verification Platform," Product Brief, Fourth Quarter 2001, 2 pages
/HE/	AP	SynaptiCAD, Inc., "PinPort links Hardware to HDL Simulations," Copyright 2001, 3 pages, available at <a href="http://www.syncad.com/pinport_main.htm?SynaptiCADSessionID=515fc200d23dcc19578a3f87f2fef366">http://www.syncad.com/pinport_main.htm?SynaptiCADSessionID=515fc200d23dcc19578a3f87f2fef366</a>

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700693-4022 D2S

EXAMINER: /Hussein El Chanti/

DATE CONSIDERED: 03/16/2008

EXAMINER: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include a copy of this form with next communication to applicant